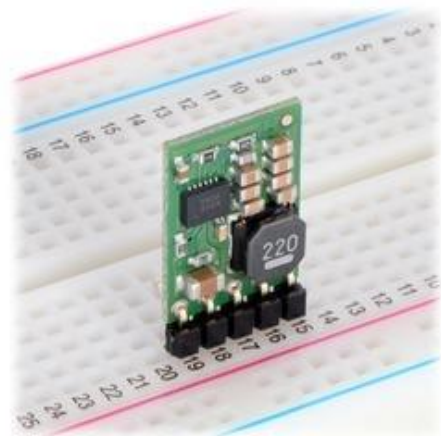
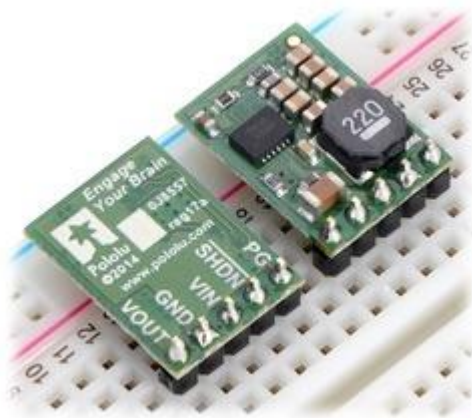


POLOLU 12V, 1A STEP-DOWN VOLTAGE
REGULATOR D24V10F12

USER'S GUIDE

CONNECTIONS

The buck regulator has five connections: power good (PG), shutdown (SHDN), input voltage (VIN), ground (GND), and output voltage (VOUT).



The “power good” indicator, **PG**, is an open-drain output that drives low when the regulator’s output voltage falls below 80% or rises above 120% of its target output voltage. This output is also actively held low for the duration of the regulator’s 2 ms soft-start period and while the regulator is being disabled by the SHDN input or by over-temperature or over-current fault conditions. An external pull-up resistor is generally required to use this pin.

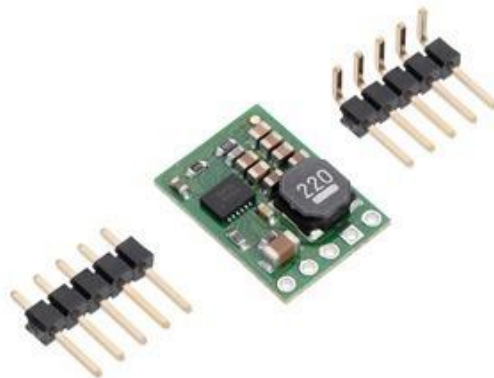
The **SHDN** pin can be driven low (under 0.4 V) to turn off the output and put the board into a low-power state. There is a 100 kΩ pull-up resistor between the SHDN pin and VIN, so if you want to leave the board permanently enabled, the SHDN pin can be left disconnected. While the SHDN pin is being driven low, the current draw of the regulator

is dominated by the current through the pull-up resistor and will be proportional to the input voltage. (At 36 V in it will draw about 360 μ A.)

The input voltage, **VIN**, powers the regulator. Voltages between 3 V and 36 V can be applied to VIN, but the effective lower limit of VIN is VOUT plus the regulator's dropout voltage, which varies approximately linearly with the load (see below for graphs of dropout voltages as a function of the load). Additionally, please be wary of destructive LC spikes (see below for more information).

The output voltage, **VOUT**, is fixed and depends on the regulator version: the D24V10F3 version outputs 3.3 V, the D24V10F5 version outputs 5 V, the D24V10F6 version outputs 6 V, the D24V10F9 version outputs 9 V, and the D24V10F12 version outputs 12 V.

The five connections are labeled on the back side of the PCB and are arranged with a 0.1" spacing along the edge of the board for compatibility with solderless breadboards, connectors, and other prototyping arrangements that use a 0.1" grid. You can solder wires directly to the board or solder in either the 5x1 [straight male header strip](#) or the 5x1 [right-angle male header strip](#) that is included.



TYPICAL EFFICIENCY AND OUTPUT CURRENT

The efficiency of a voltage regulator, defined as $(\text{Power out})/(\text{Power in})$, is an important measure of its performance, especially when battery life or heat are concerns. This family of switching regulators typically has an efficiency of 80% to 93%, though the actual efficiency in a given system depends on input voltage, output voltage, and output current. See the efficiency graph near the bottom of this page for more information.

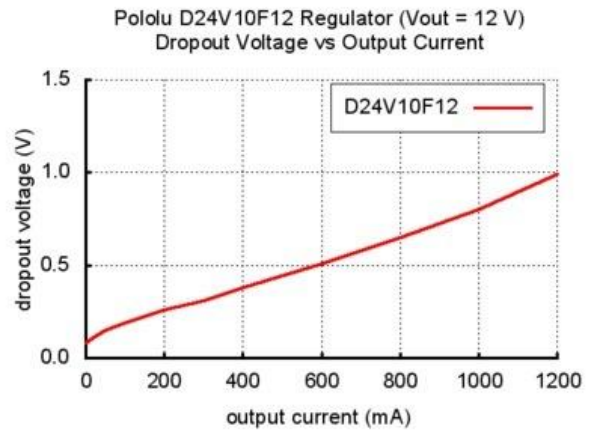
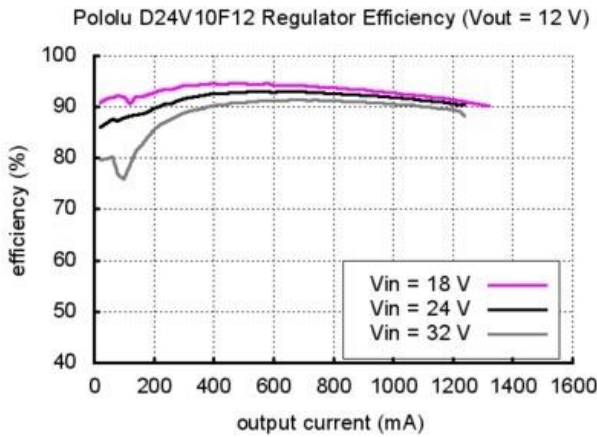
In order to achieve a high efficiency at low loads, this regulator automatically goes into a power-save mode where the switching frequency is reduced. In power-save mode, the switching frequency of the regulator changes as necessary to minimize power loss. This could make it harder to filter out noise on the output caused by switching.

TYPICAL DROPOUT VOLTAGE

The dropout voltage of a step-down regulator is the minimum amount by which the input voltage must exceed the regulator's target output voltage in order to ensure the target output can be achieved. For example, if a 5 V regulator has a 1 V dropout voltage, the input must be at least 6 V to ensure the output is the full 5 V. Generally speaking, the dropout voltage increases as the output current increases. See the "Details" section below for more information on the dropout voltage for this specific regulator version.

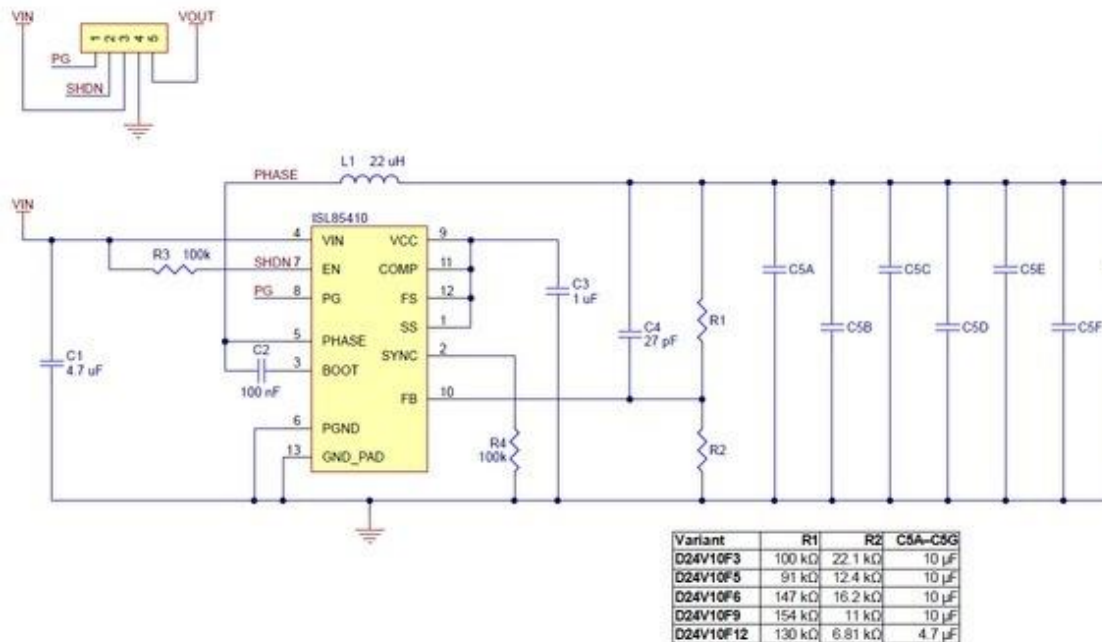
DETAILS FOR ITEM #2834

The graphs below show the typical efficiency and dropout voltage of the 12 V D24V10F12 regulator as a function of the output current:



During normal operation, this product can get hot enough to burn you. Take care when handling this product or other components connected to it

SCHEMATIC DIAGRAM



Schematic diagram for the Pololu D24V10Fx family of 1 A step-down voltage regulators.

This schematic is also available as a [downloadable pdf](#) (173k pdf).

LC VOLTAGE SPIKES

When connecting voltage to electronic circuits, the initial rush of current can cause voltage spikes that are much higher than the input voltage. If these spikes exceed the regulator's maximum voltage (36 V), the regulator can be destroyed. In our tests with typical power leads (~30" test clips), input voltages above 20 V caused spikes over 36 V.

If you are connecting more than 20 V or your power leads or supply has high inductance, we recommend soldering a 33 μ F or larger electrolytic capacitor close to the regulator between VIN and GND. The capacitor should be rated for at least 50 V.